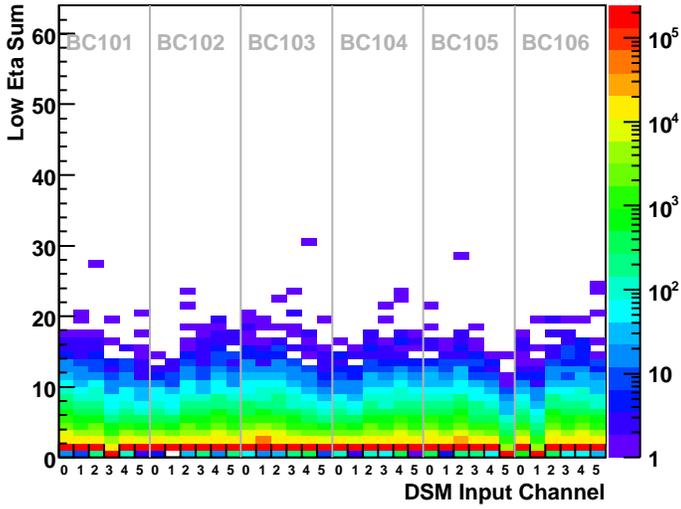


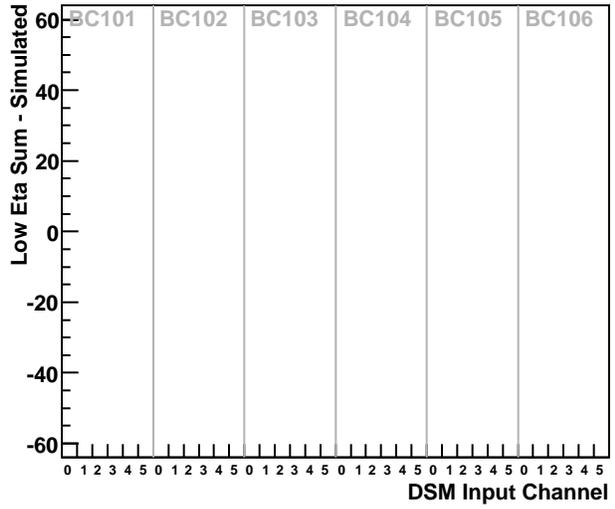
Barrel EMC L1 Input - Low Eta Sum

Entries 9000000



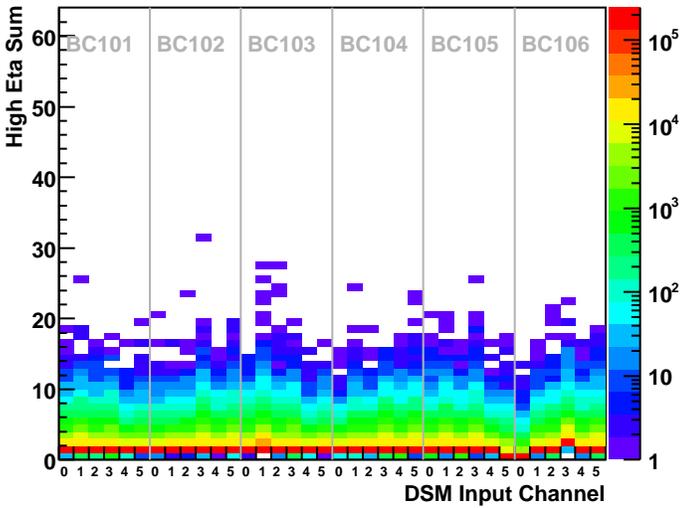
Barrel EMC L1 Input - Low Eta Sum

Entries 0



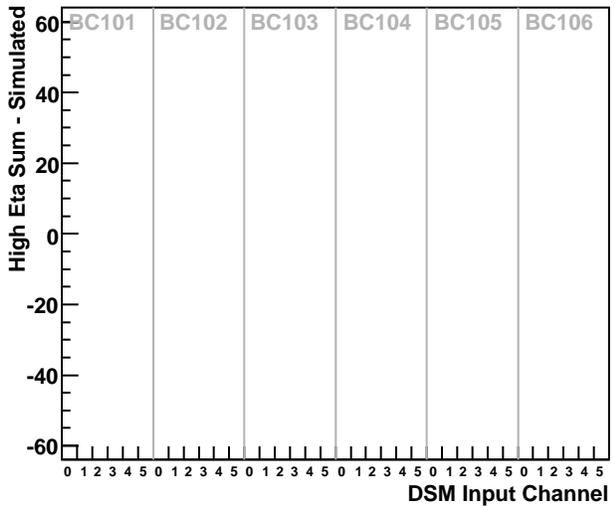
Barrel EMC L1 Input - High Eta Sum

Entries 9000000



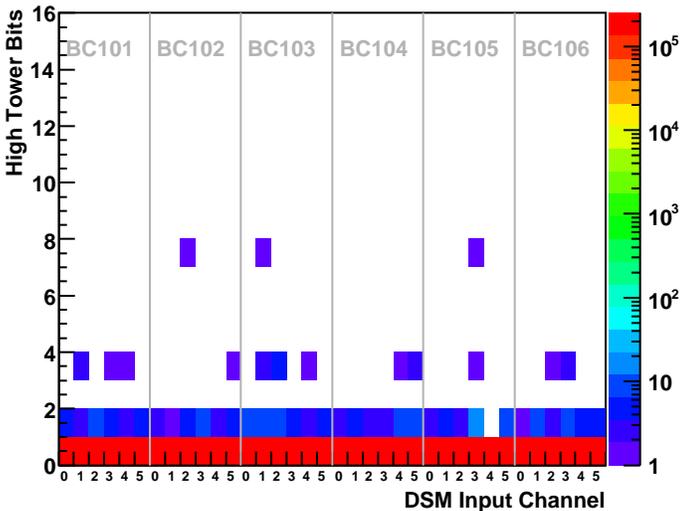
Barrel EMC L1 Input - High Eta Sum

Entries 0



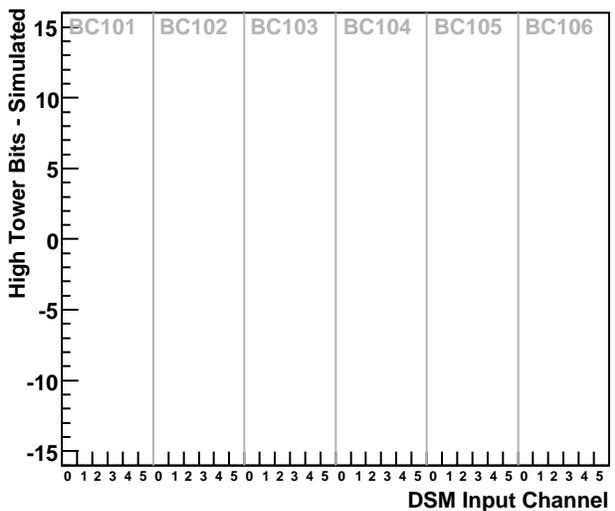
Barrel EMC L1 Input - High Tower Bits

Entries 9000000



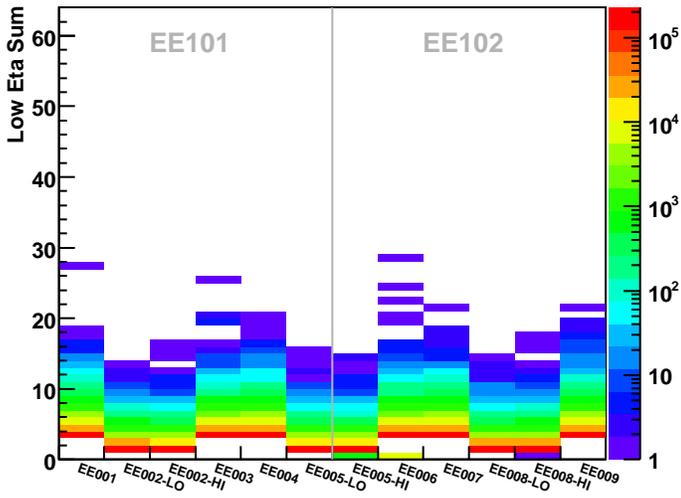
Barrel EMC L1 Input - High Tower Bits

Entries 0



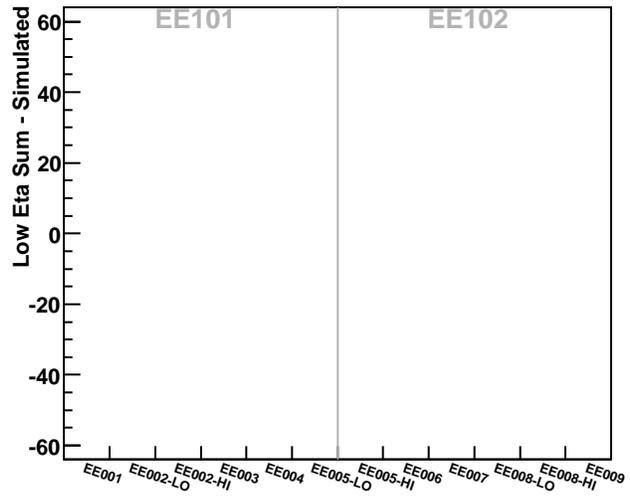
Endcap EMC L1 Input - Low Eta Sum

Entries 3000000



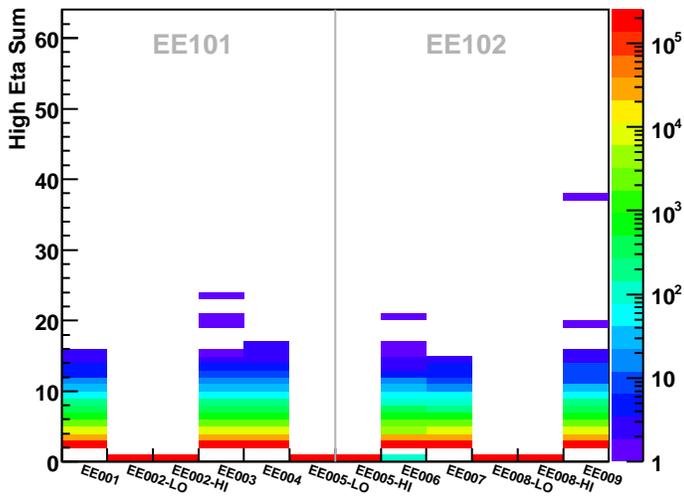
Endcap EMC L1 Input - Low Eta Sum

Entries 0



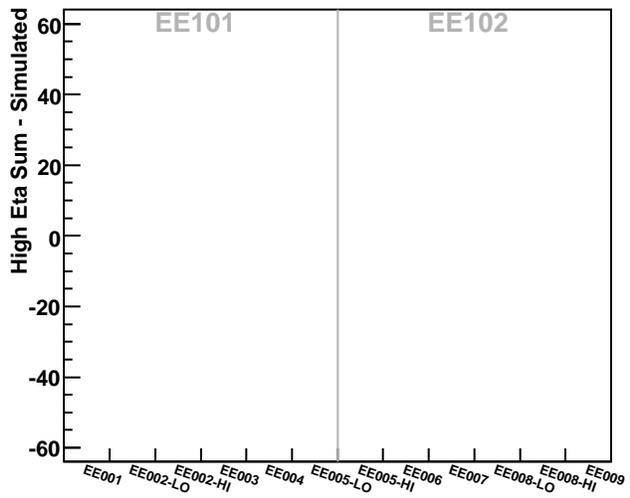
Endcap EMC L1 Input - High Eta Sum

Entries 3000000



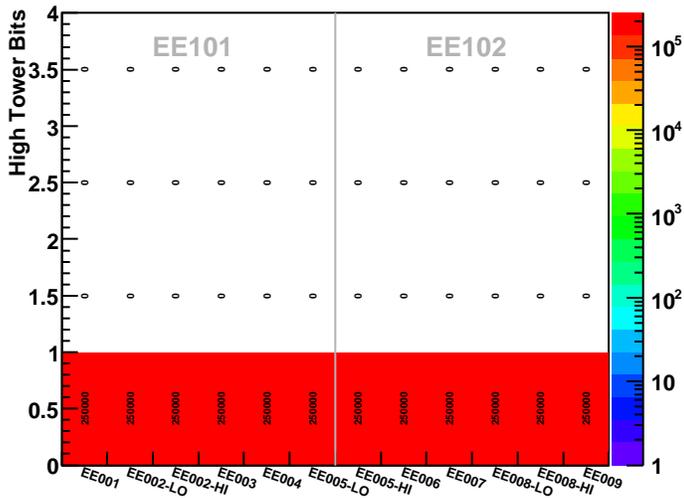
Endcap EMC L1 Input - High Eta Sum

Entries 0



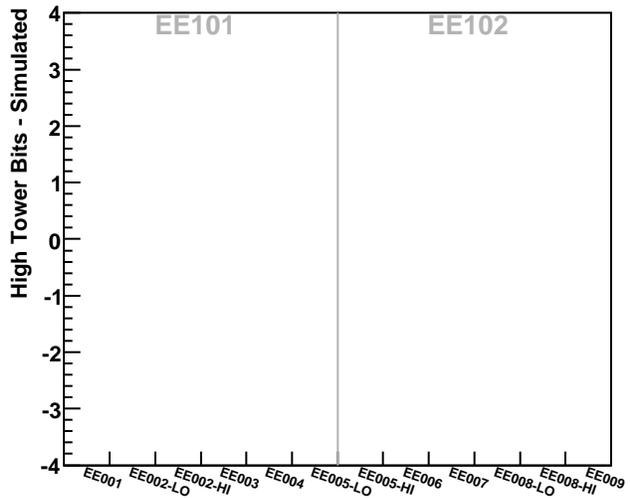
Endcap EMC L1 Input - High Tower Bits

Entries 3000000



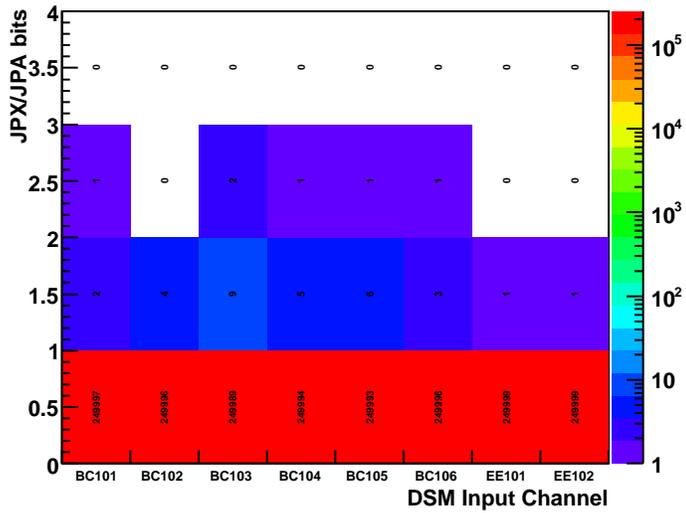
Endcap EMC L1 Input - High Tower Bits

Entries 0



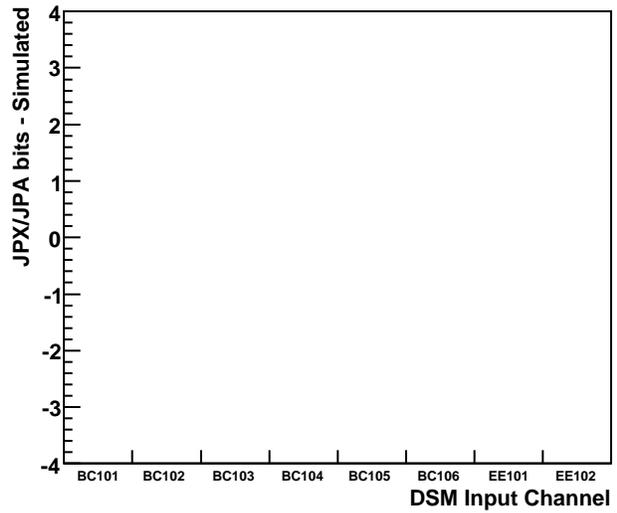
EMC L2 Input - JPX/JPA bits

Entries 2000000



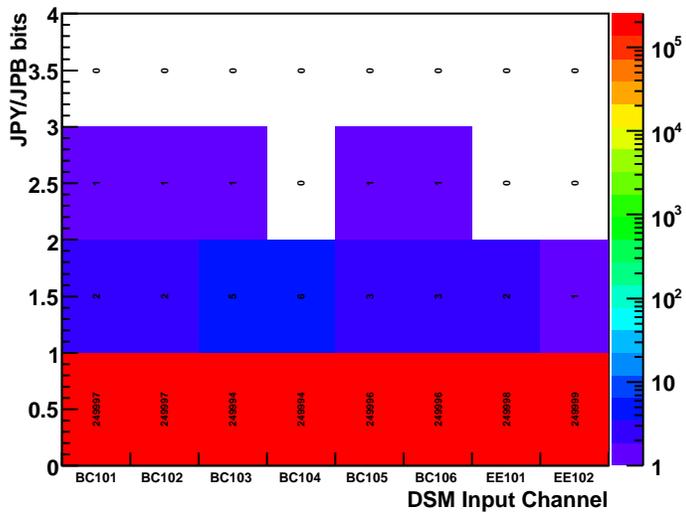
EMC L2 Input - JPX/JPA bits

Entries 0



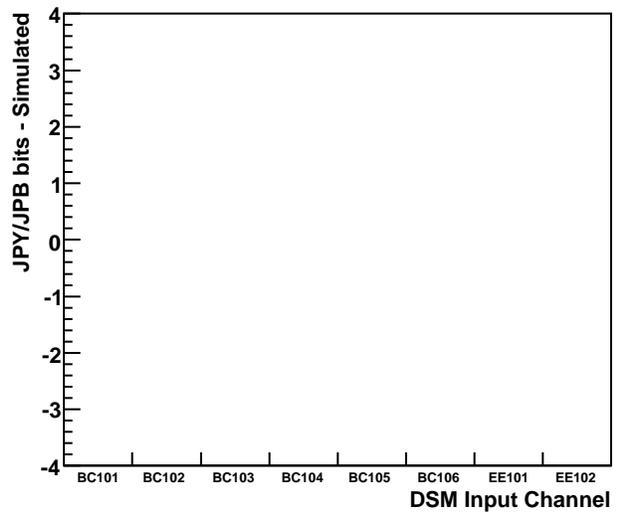
EMC L2 Input - JPY/JPB bits

Entries 2000000



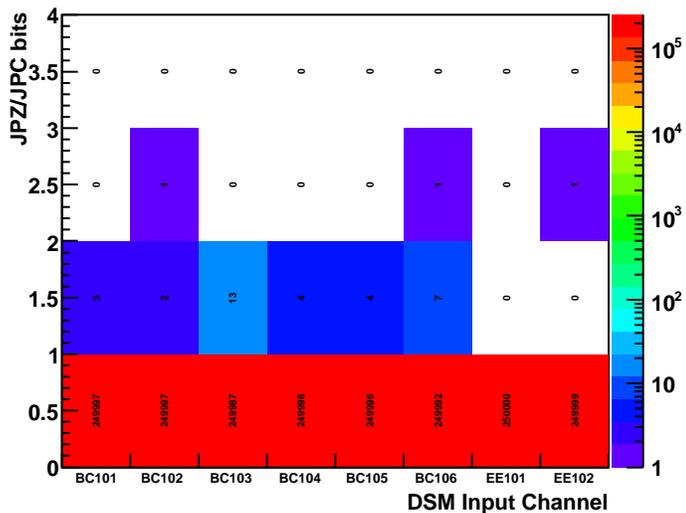
EMC L2 Input - JPY/JPB bits

Entries 0



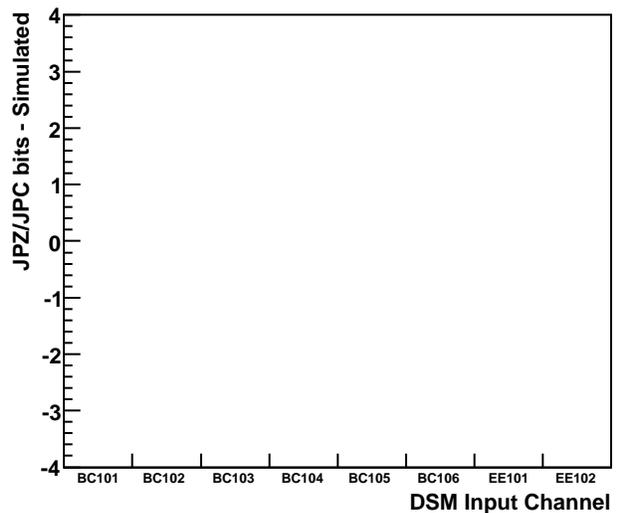
EMC L2 Input - JPZ/JPC bits

Entries 2000000



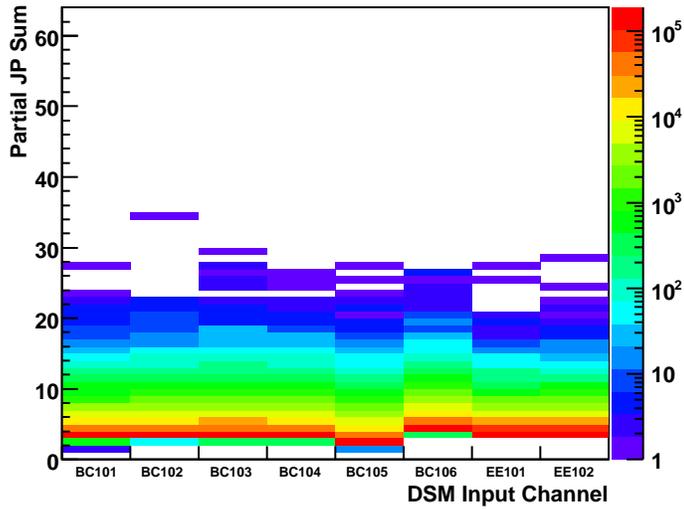
EMC L2 Input - JPZ/JPC bits

Entries 0



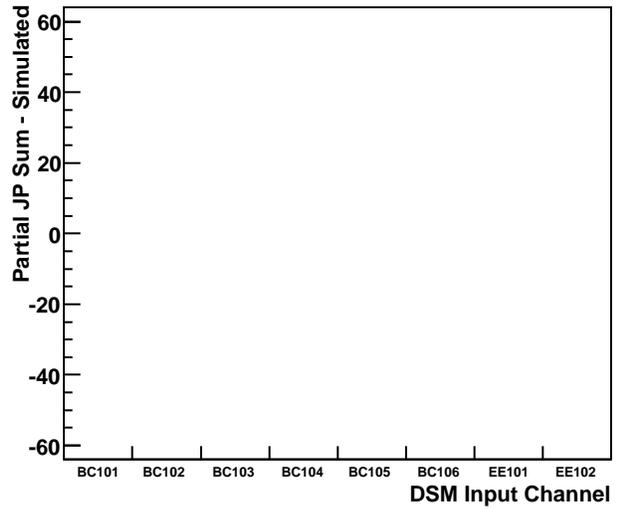
EMC L2 Input - Partial JP Sum

Entries 2000000



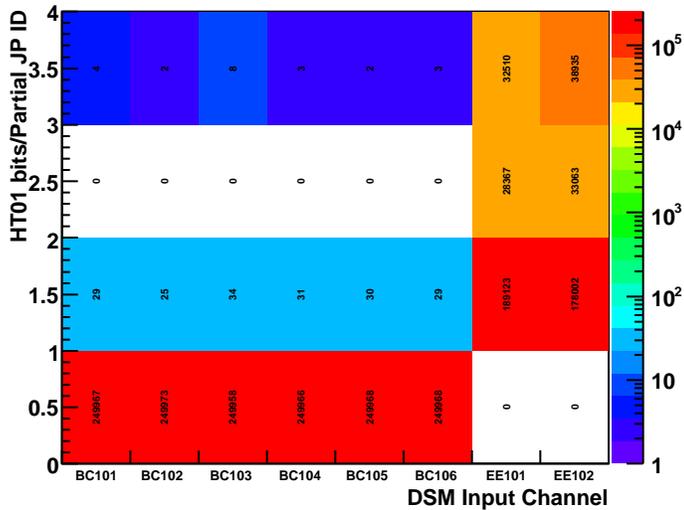
EMC L2 Input - Partial JP Sum

Entries 0



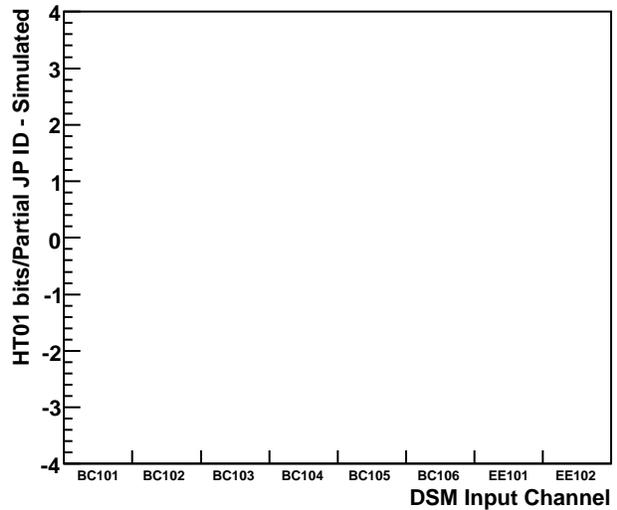
EMC L2 Input - HT01 bits/Partial JP ID

Entries 2000000



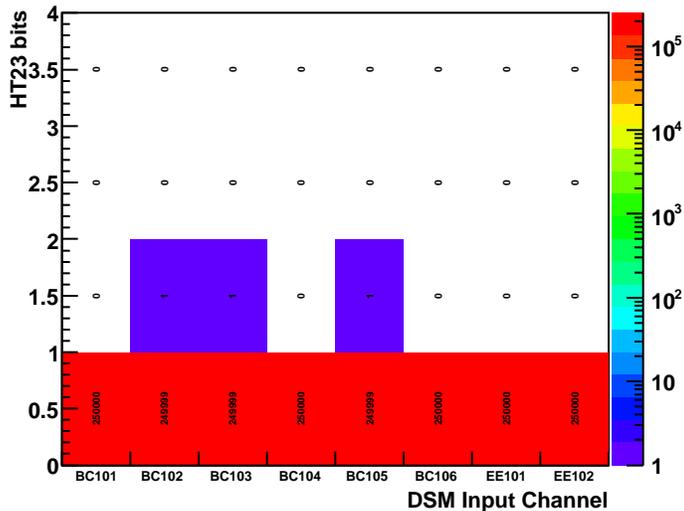
EMC L2 Input - HT01 bits/Partial JP ID

Entries 0



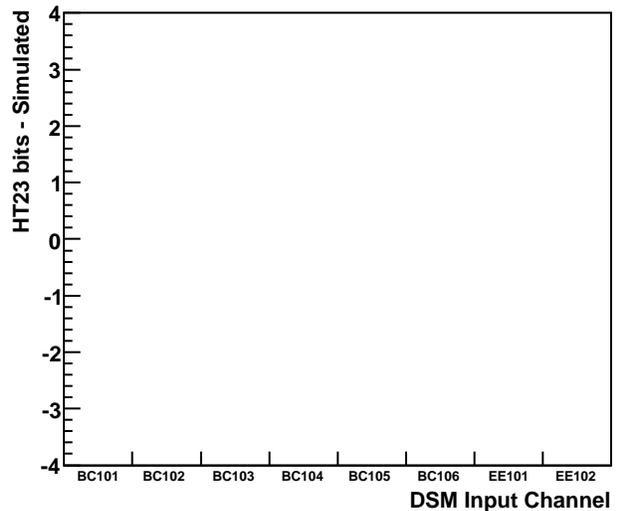
EMC L2 Input - HT23 bits

Entries 2000000



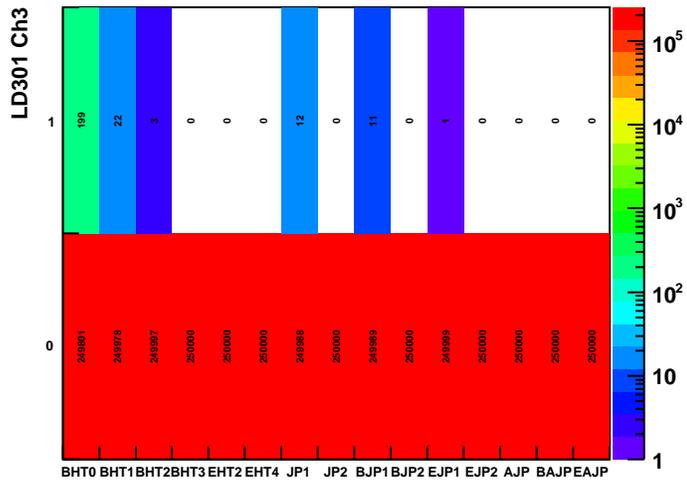
EMC L2 Input - HT23 bits

Entries 0



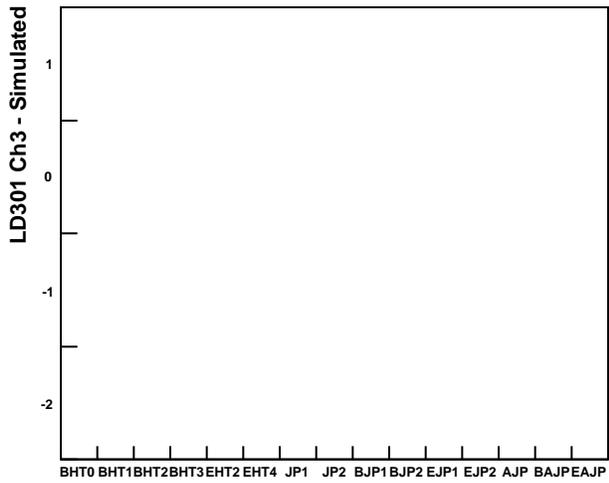
Last DSM Ch3 Input

Entries 3750000



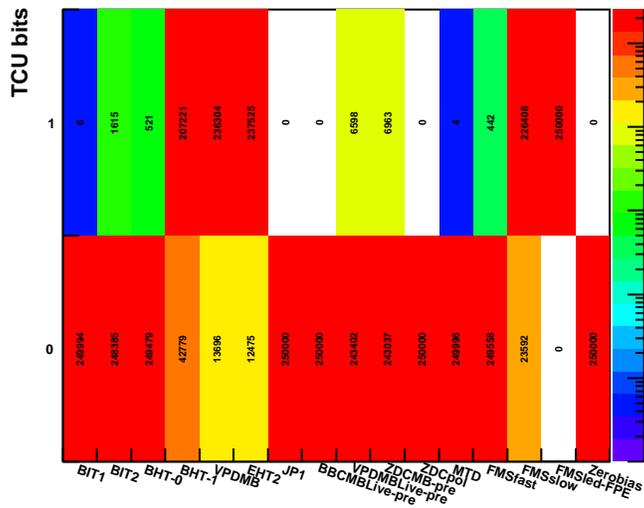
Last DSM Ch3 Input

Entries 0



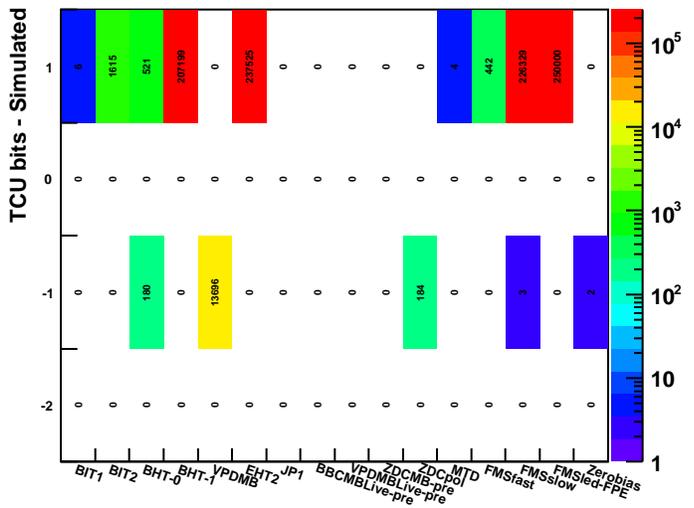
TCU bits

Entries 4000000



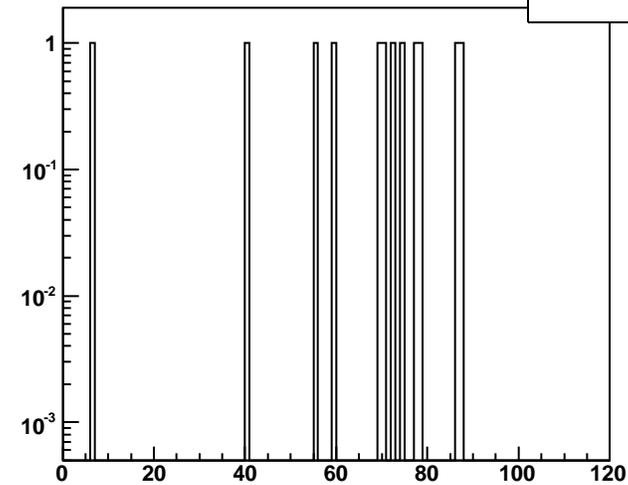
TCU bits

Entries 937706



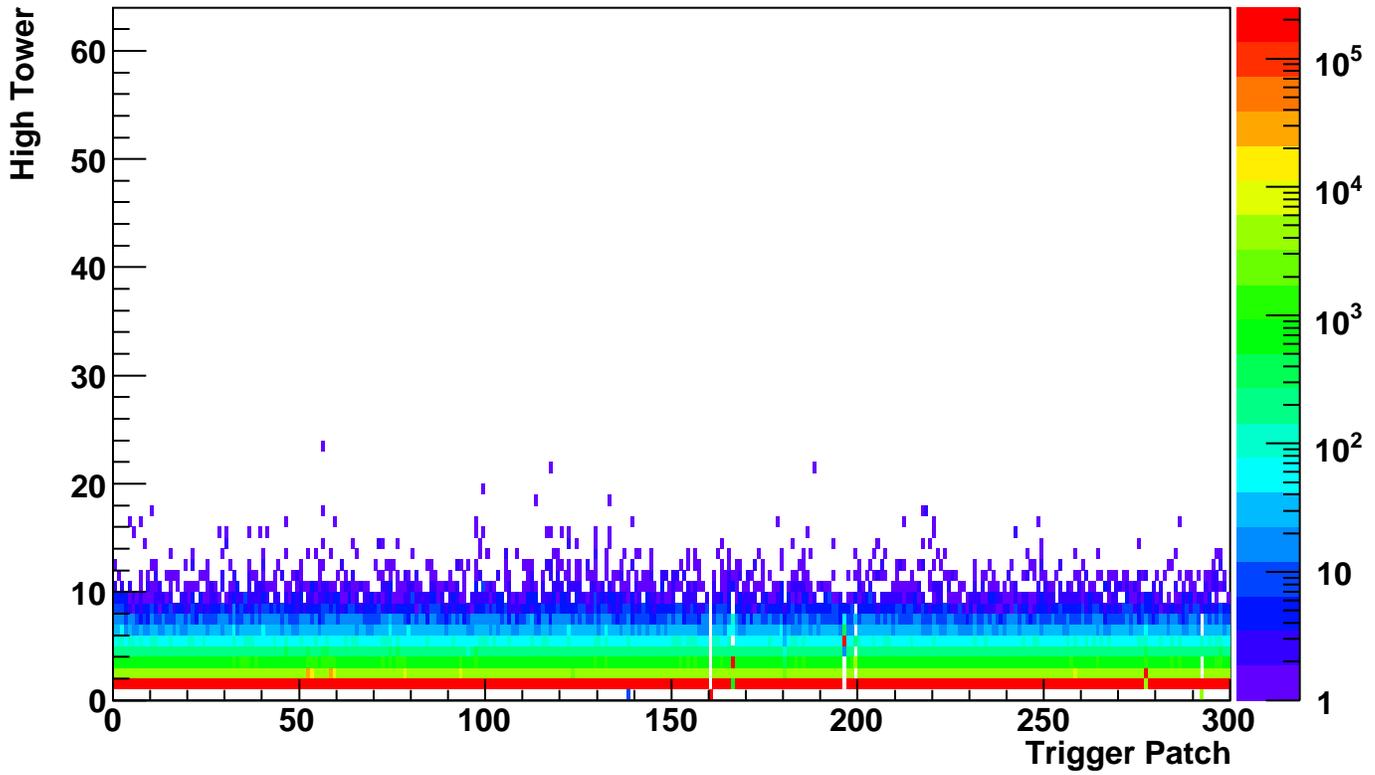
BunchId7Bit (JP1)

Entries 12



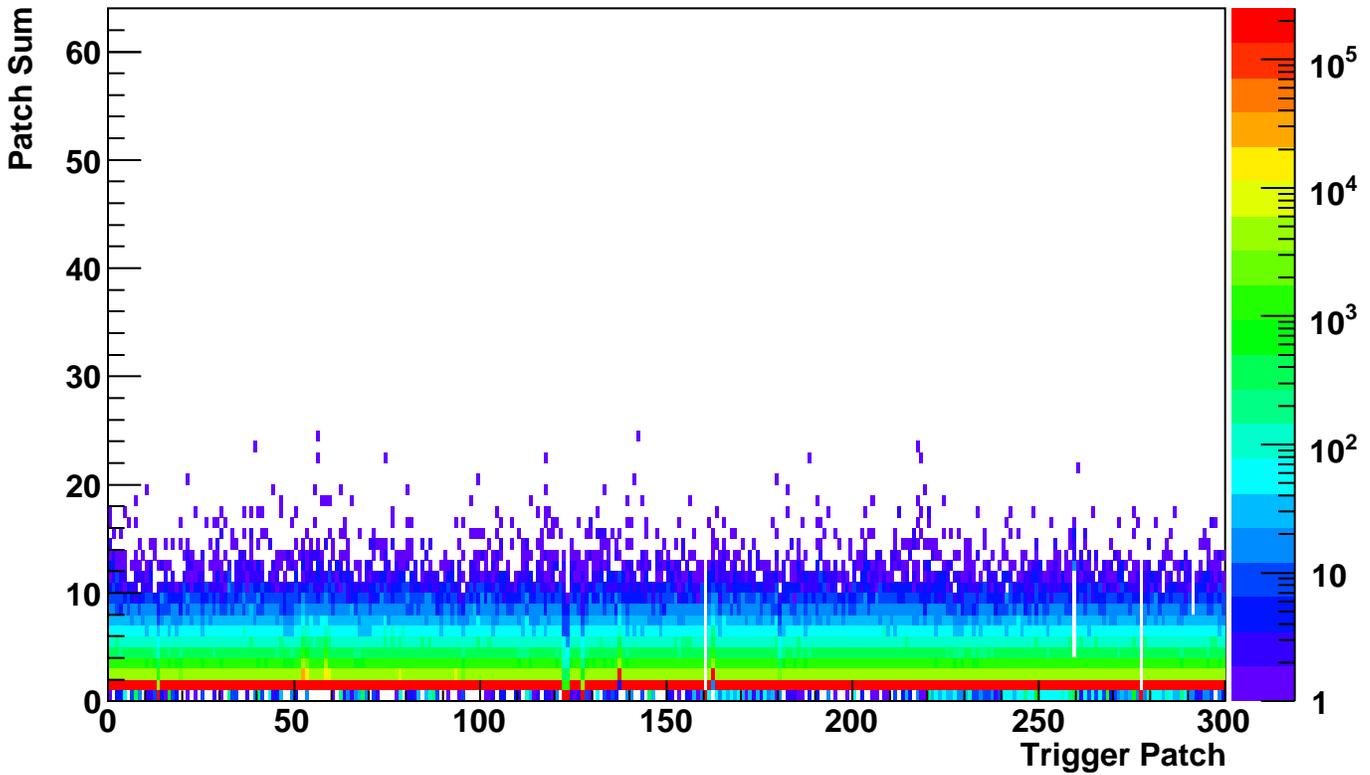
Barrel EMC L0 Input - High Tower

Entries 7.5e+07



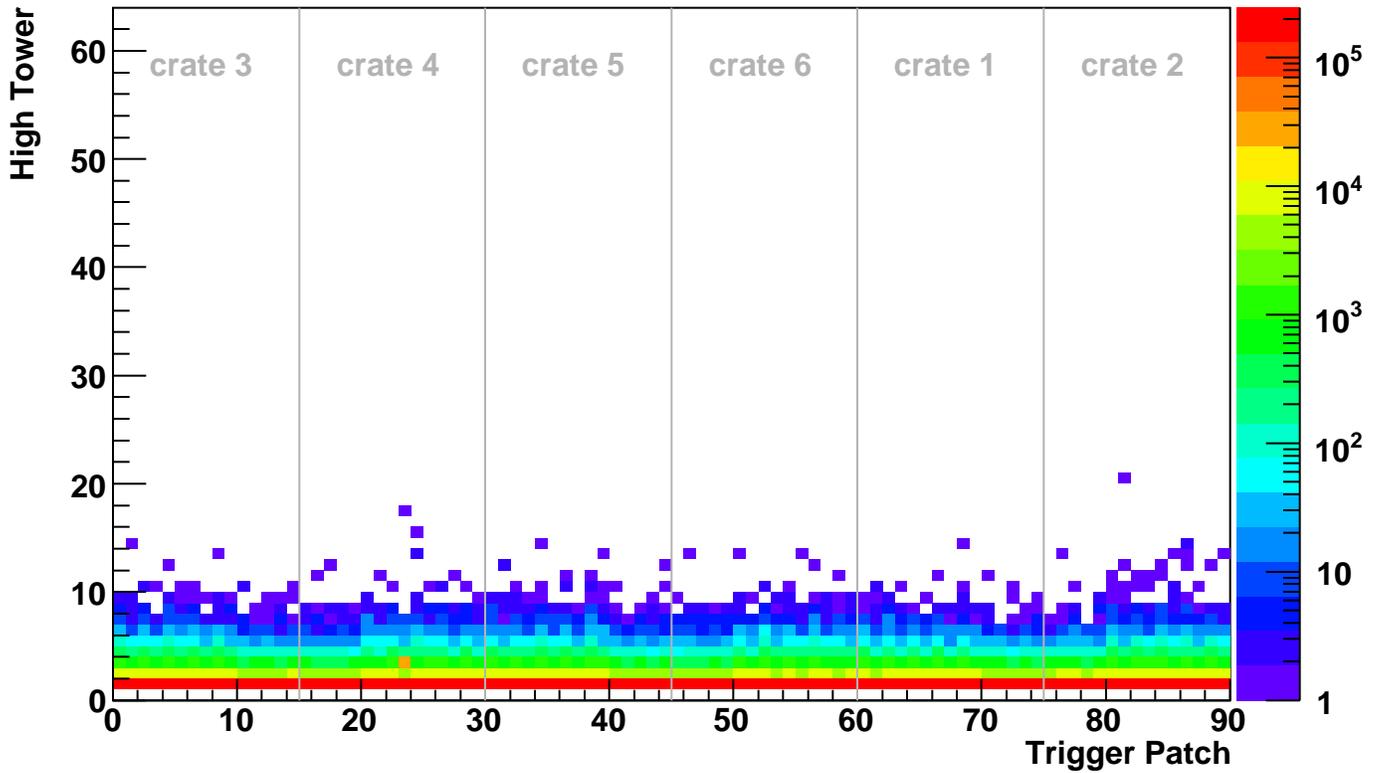
Barrel EMC L0 Input - Patch Sum

Entries 7.5e+07



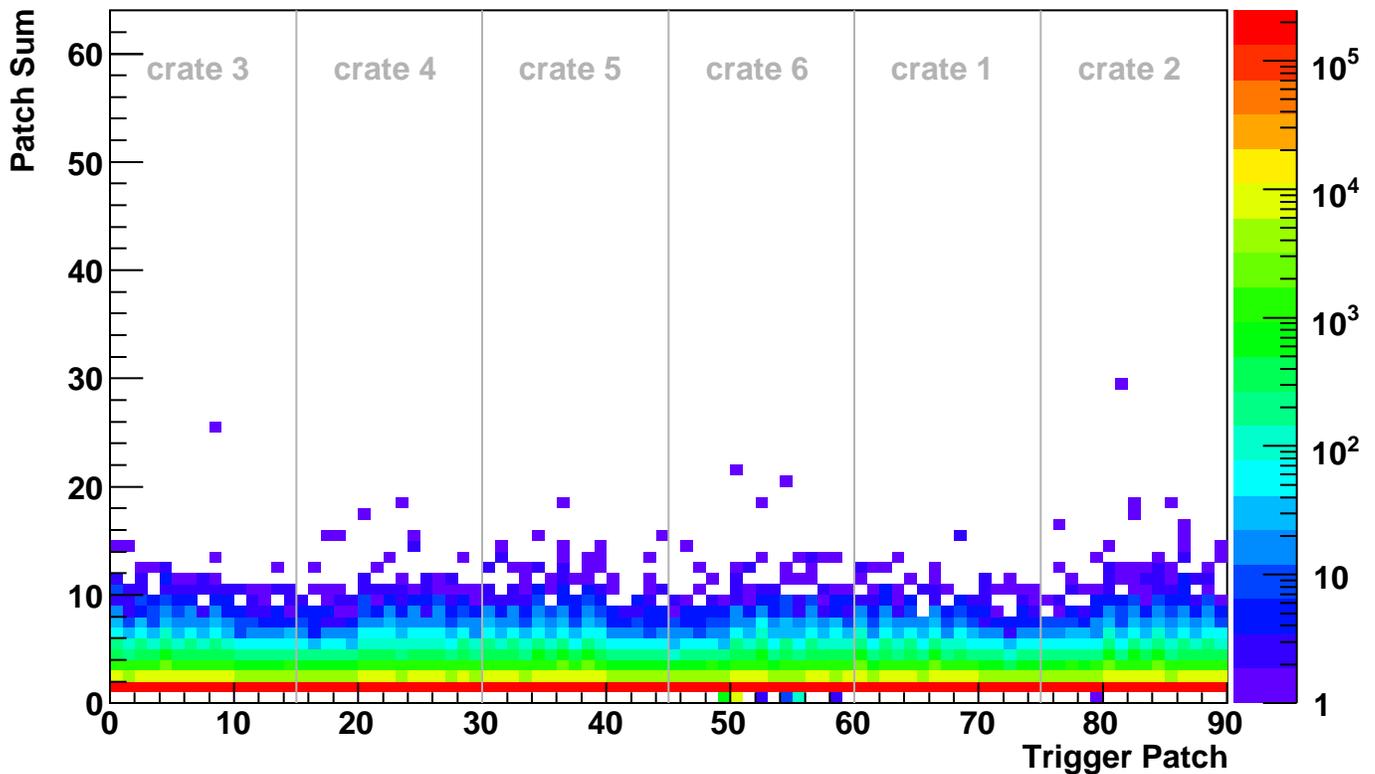
Endcap EMC L0 Input - High Tower

Entries 2.25e+07



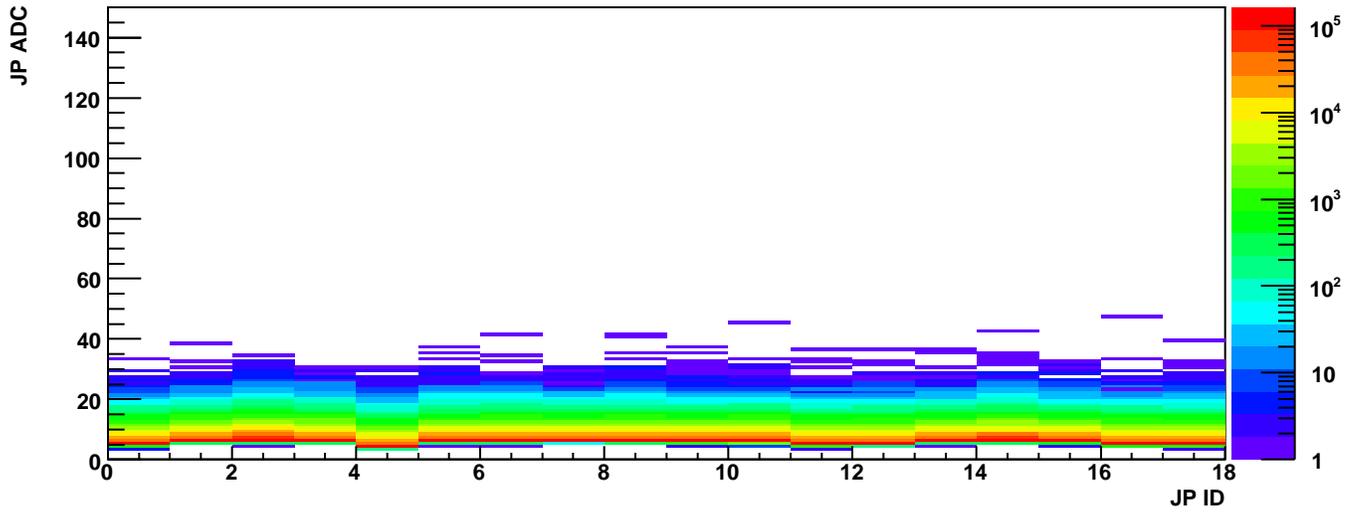
Endcap EMC L0 Input - Patch Sum

Entries 2.25e+07



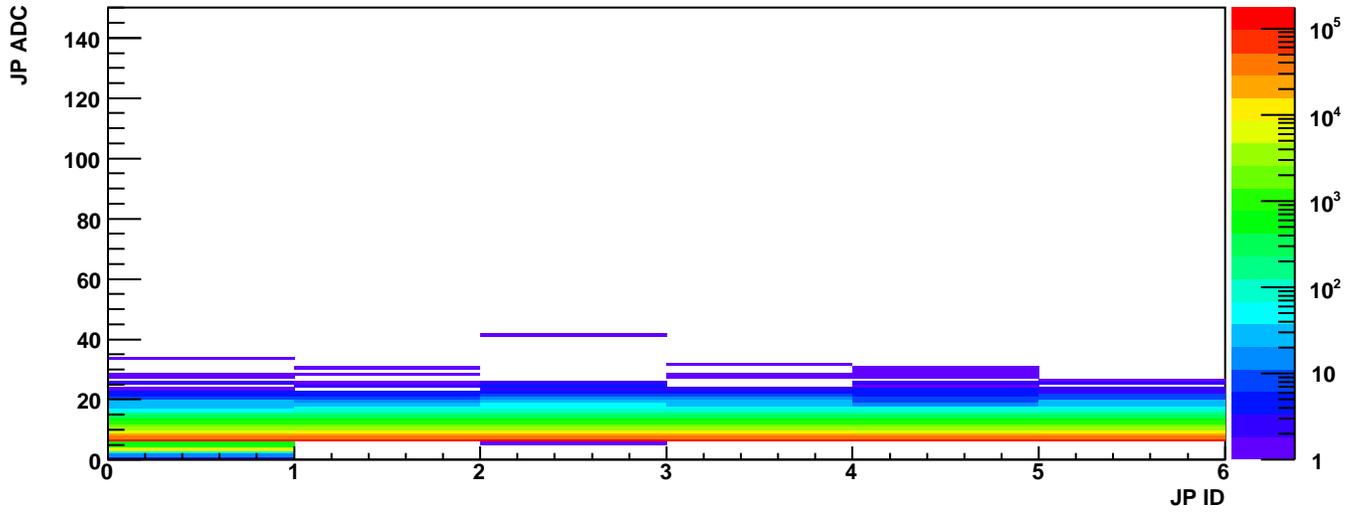
Barrel Jet Patches

Entries 450000



Endcap Jet Patches

Entries 150000



Hybrid Jet Patches

Entries 50000

